Chapter 118
The What, How, and When of Formal Methods

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ABSTRACT

Questions such as what are formal methods, how are formal methods implemented, how are they used in software engineering, and when should they be used, among other related questions are the main objective of this chapter. Some definitions are given to answer some of these questions; the chapter also states the aims of FM as well as giving their main characteristics. An example that shows how formal methods can be used for specifying not only software requirements but also the rest of the stages in a software development process is given. A discussion about when they should be used, explaining the reasons why they should be applied when security and reliability are important requirements of the software under development, is presented. Finally, some arguments about how they can also be used as a complement to traditional development methods are provided.

INTRODUCTION

Formal Methods (FM) is an area of Software Engineering. They comprise a collection of methodologies and related tools, employing a mathematical basis –as do most engineering disciplines–, to construct its products. Although FM are part of Software Engineering, they extend their scope to the development not only of software products but also of hardware systems.

The goal of this article is to give answers to questions such as *what are Formal Methods*, *how are Formal Methods implemented*, *how are they used in Software Engineering*, *when should they be used*, among other related questions.

The chapter starts answering the question of *what are FM*; also the aims of FM are stated at the same time that their main characteristics are presented.

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An example that shows *how* FM can be used to help specifying software requirements as well as the rest of the stages in a software development process is given as answers to the questions *how are FM implemented* and *how FM are used in Software Engineering*.

A discussion about *when they should be used*, explaining why they should be employed when the software system is required to be as secure and reliable as possible and how they can also be used as a complement to traditional development methods, is also provided.

A section on the state of the art in FM, providing an analysis of Lightweight FM and the growing impact that Model Checking is having in the software and hardware industry as an automatic FM for system verification, is presented. Finally, a discussion on the use of automatic analyzers like Alloy, which replace conventional analysis based in theorem proof by a "non-complete" analysis based in the examination of cases, is also given.

BACKGROUND

The 'What' of FM

What Are Formal Methods

FM contain a wide range of methods and related tools oriented to the production of secure and reliable software and hardware systems by employing a logic-mathematical basis.

As traditional development methods, FM consist of a set of techniques and supporting tools to assists developers during the whole software development process. The fundamental difference with traditional methods is that FM are based on mathematics and formal logic leading to unambiguous specifications, where desired properties of a system under development can be formally expressed and verified.

The adoption of FM makes possible the specification and the verification of software and hardware systems. They provide the mathematical tools to develop new concrete formal specifications and, eventually, executable code from abstract formal specifications, where all the development steps can be formally verified.

Therefore, in contrast to traditional development methods, FM use mathematical proofs as a complement to software testing in order to verify the correctness of the system under development.

There are a number of different Formal Methods, each having its own notation, methodology and supporting tools; a comprehensive list of FM can be found in Formal Methods Wiki (Bowen). Formal notations or formal specification languages are used to produce formal specifications of software and hardware systems. There are different styles in formal notations, and there also are different degrees of rigor in development. Formal specifications can be written either using *abstract* or *concrete* style. Some formal specification languages adopt a *property-oriented* style, allowing the creation of *algebraic specifications*, where the desired properties of the system under development are given by axioms, in a purely declarative way. This kind of specifications is called *algebraic* because specifications are seen like heterogeneous algebras. A different style for specifications is the use of *model-based* notations, which make use of concrete data types (integers, reals, sets, list, etc.). They are more concrete than property-oriented specifications. However, in general, formal specification languages favors abstraction, being oriented to answer the question *what are the software requirements* of a system more than *how*